

**UNIVERSITY COLLEGE OF ENGINEERING (Autonomous)
OSMANIA UNIVERSITY, HYDERABAD**

**M.E. (E.C.E) II - Semester (Regular & PTPG) and IV-Semester PTPG (Main) Examination
July 2018**

**Examination Centre:
Department of ECE,
UCE (A), O.U.**

REVISED EXAMINATION TIME TABLE

Time : 2.00 PM to 5.00 PM

Date and Day	Digital Systems			Systems & Signal Processing	Microwave & Radar Engineering		Embedded Systems and VLSI Design
	Regular II-Semester	PTPG II-Semester	PTPG IV-Semester	Regular II-Semester	Regular II-Semester	PTPG IV-Semester	Regular II-Semester
02-07-2018 MONDAY	ASIC Verification with System Verilog	ASIC Verification with System Verilog	=====	Image and Video Processing	Electromagnetic Interference and Compatibility	Electromagnetic Interference and Compatibility	VLSI Physical Design
04-07-2018 WEDNESDAY	Advanced Communication & Computer Networks	Advanced Communication & Computer Networks	=====	Satellite Navigation Systems	Microwave Integrated Circuits	=====	Design for Testability
06-07-2018 FRIDAY	Low Power VLSI Design	=====	Optical Fiber Communication Systems	Optical Fiber Communication Systems	Optical Fiber Communication Systems	=====	VLSI Technology
09-07-2018 MONDAY	Operating System for Embedded Systems	=====	Operating System for Embedded Systems	Radar Signal Processing	Radar Signal Processing	Radar Signal Processing	Operating System for Embedded Systems
11-07-2018 WEDNESDAY	VLSI Design & Technology	VLSI Design & Technology	=====	Adaptive Signal Processing	Radar Systems Engineering	=====	Embedded System Programming
13-07-2018 FRIDAY	Scripting Languages for VLSI Design Automation	=====	Scripting Languages for VLSI Design Automation	Digital Spectral Analysis	Satellite Radio Navigation	=====	Software Defined Radio
16-07-2018 MONDAY	Engineering Research Methodology						
18-07-2018 WEDNESDAY	=====	=====	=====	Optimization Techniques	=====	Phased Array Radar	=====

Note: This Time-Table is also applicable for Pre-Ph.D. students.


**DIRECTOR OF EVALUATION
EXAMINATION CELL**